

FIG. 1

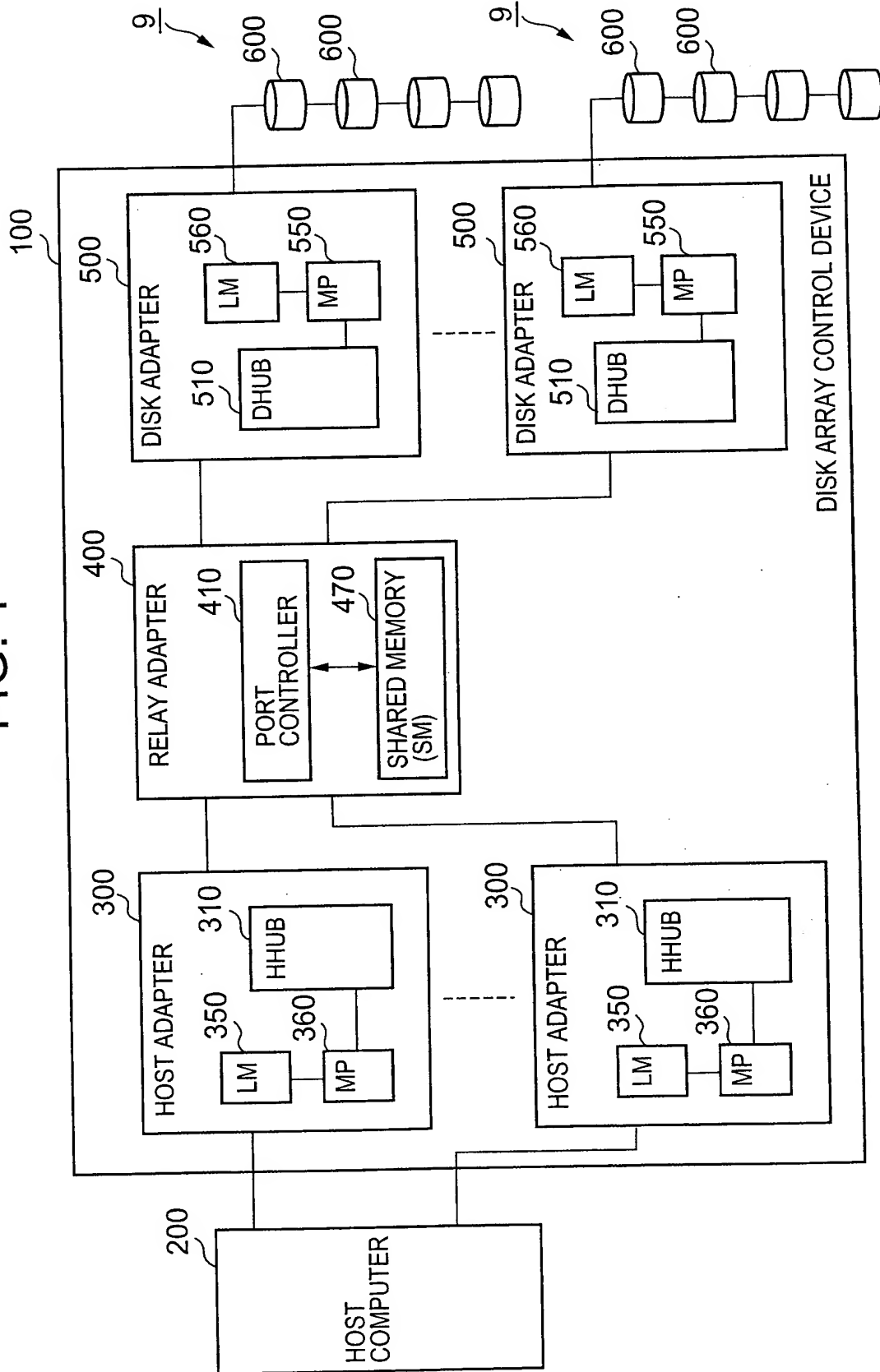


FIG. 2

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OTHER MP 550 (MP # "08")	
ADDRESS	USE
0x0000 0000	PCIX OR OTHER DEVICE
0x1000 0000	SMA I/O REGISTER
0x2000 0000	TRANSMISSION SM ADDRESS INFORMATION
0x3000 0000	RECEPTION SM ADDRESS INFORMATION
0x4000 0000	LM ADDRESS INFORMATION FOR OTHER MP
0x5000 0000	LM ADDRESS INFORMATION FOR OWN MP
0x50FF FFFF	

ADDRESS	NAME OF ADAPTER	OTHER MP #
0x5000 0000	CHA0	00
0x5100 0000		01
0x5200 0000	CHA1	02
0x5300 0000		03
0x5400 0000	CHA2	04
0x5500 0000		05
0x5600 0000	CHA3	06
0x5700 0000		07
0x5800 0000	DKA0	08
0x5900 0000		09
0x5A00 0000	DKA1	0a
0x5B00 0000		0b
0x5C00 0000	DKA2	0c
0x5D00 0000		0d
0x5E00 0000	DKA3	0e
0x5F00 0000		0f

ADDRESS	USE
0x0000 0000	PCIX OR OTHER DEVICE
0x1000 0000	SMA I/O REGISTER
0x2000 0000	TRANSMISSION SM ADDRESS INFORMATION
0x3000 0000	RECEPTION SM ADDRESS INFORMATION
0x4000 0000	LM ADDRESS INFORMATION FOR OTHER MP
0x5000 0000	LM ADDRESS INFORMATION FOR OWN MP

START	NAME OF ADAPTER	OTHER MP #	OTHER LM ADDRESS
0x4000 0000	CHA0	00	0x0000 0000
0x4100 0000		01	0x0000 0000
0x4200 0000		02	0x0000 0000
0x4300 0000		03	0x0000 0000
0x4400 0000	CHA2	04	0x0000 0000
0x4500 0000		05	0x0000 0000
0x4600 0000	CHA3	06	0x0000 0000
0x4700 0000		07	0x0000 0000
0x4800 0000	DKA0	08	0x0000 0000
0x4900 0000		09	0x0000 0000
0x4A00 0000	DKA1	0a	0x0000 0000
0x4B00 0000		0b	0x0000 0000
0x4C00 0000	DKA2	0c	0x0000 0000
0x4D00 0000		0d	0x0000 0000
0x4E00 0000	DKA3	0e	0x0000 0000
0x4F00 0000		0f	0x0000 0000

FIG. 3

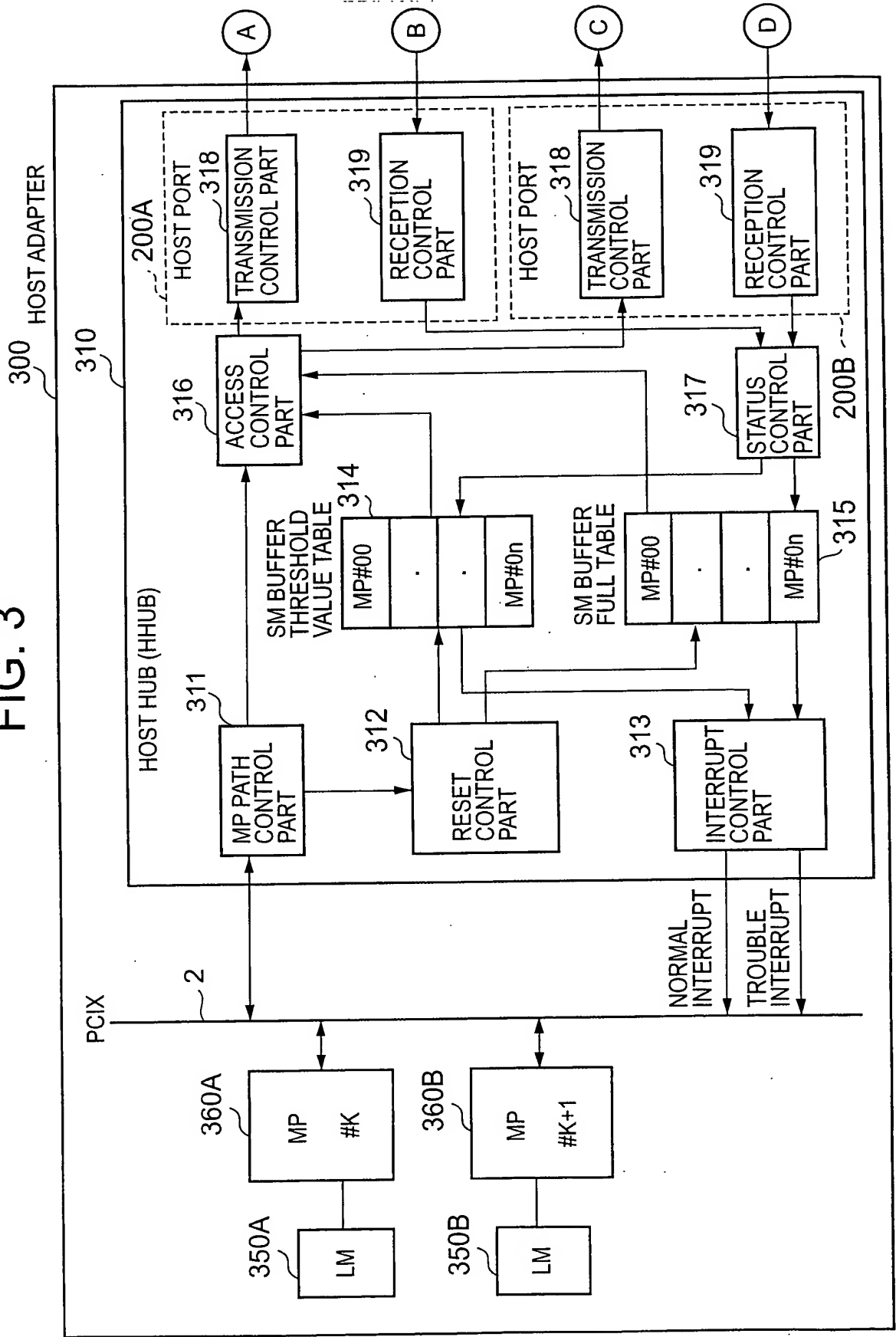


FIG. 4

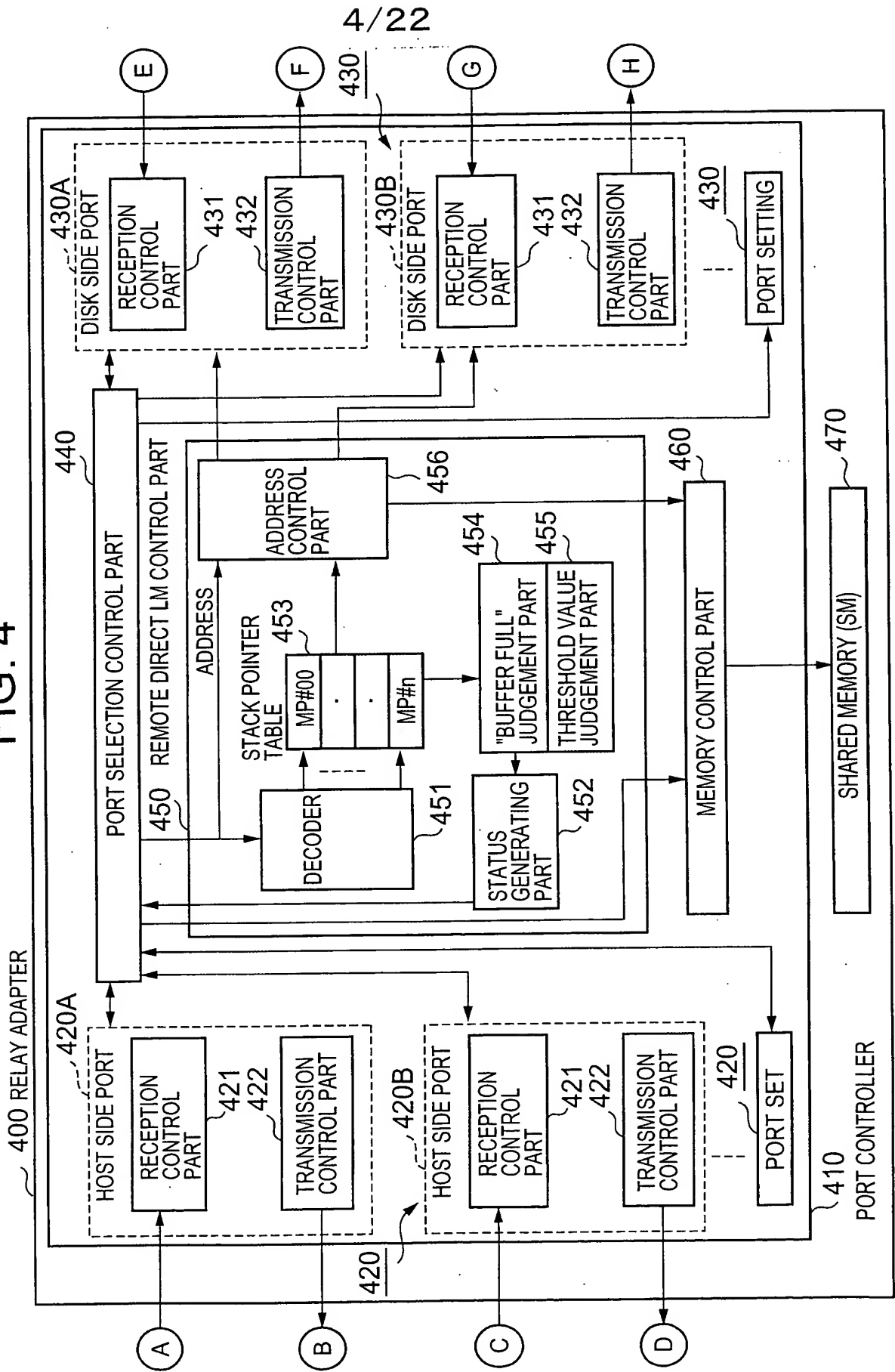


FIG. 5

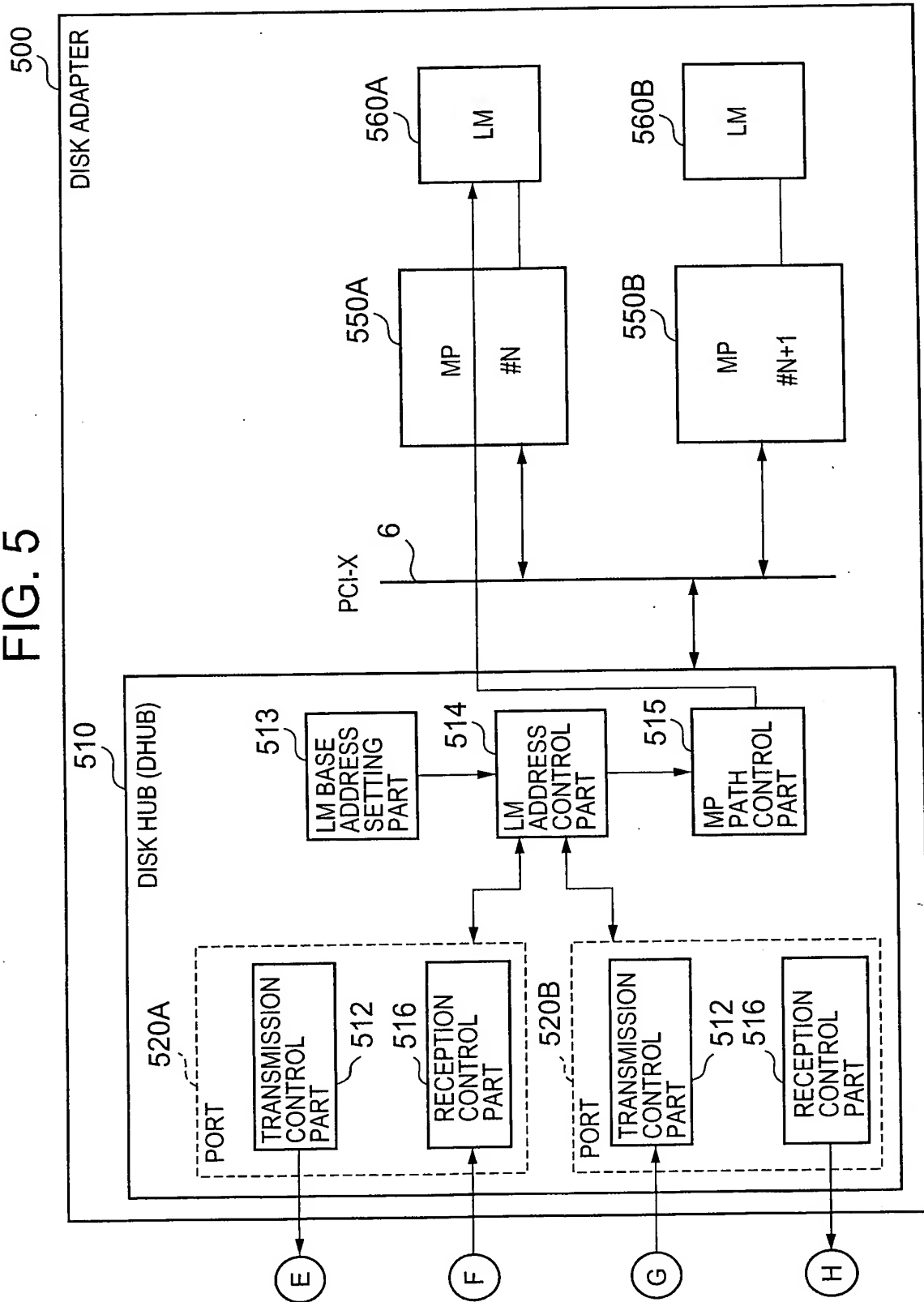


FIG. 6

ADDRESS INFORMATION	COMMAND TYPE	WRITE DATA	CRC
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FIG. 7

DESTINATION MP DISCRIMINATING INFORMATION	BUFFER FULL	OVER BUFFER THRESHOLD VALUE	BUFFER EMPTY	PATH ERROR	PORT CONTROLLER INTERNAL ERROR	SEQUENCE NO.	CRC
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FIG. 8

ADDRESS INFORMATION	COMMAND TYPE	WRITE DATA	CRC
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FIG. 9

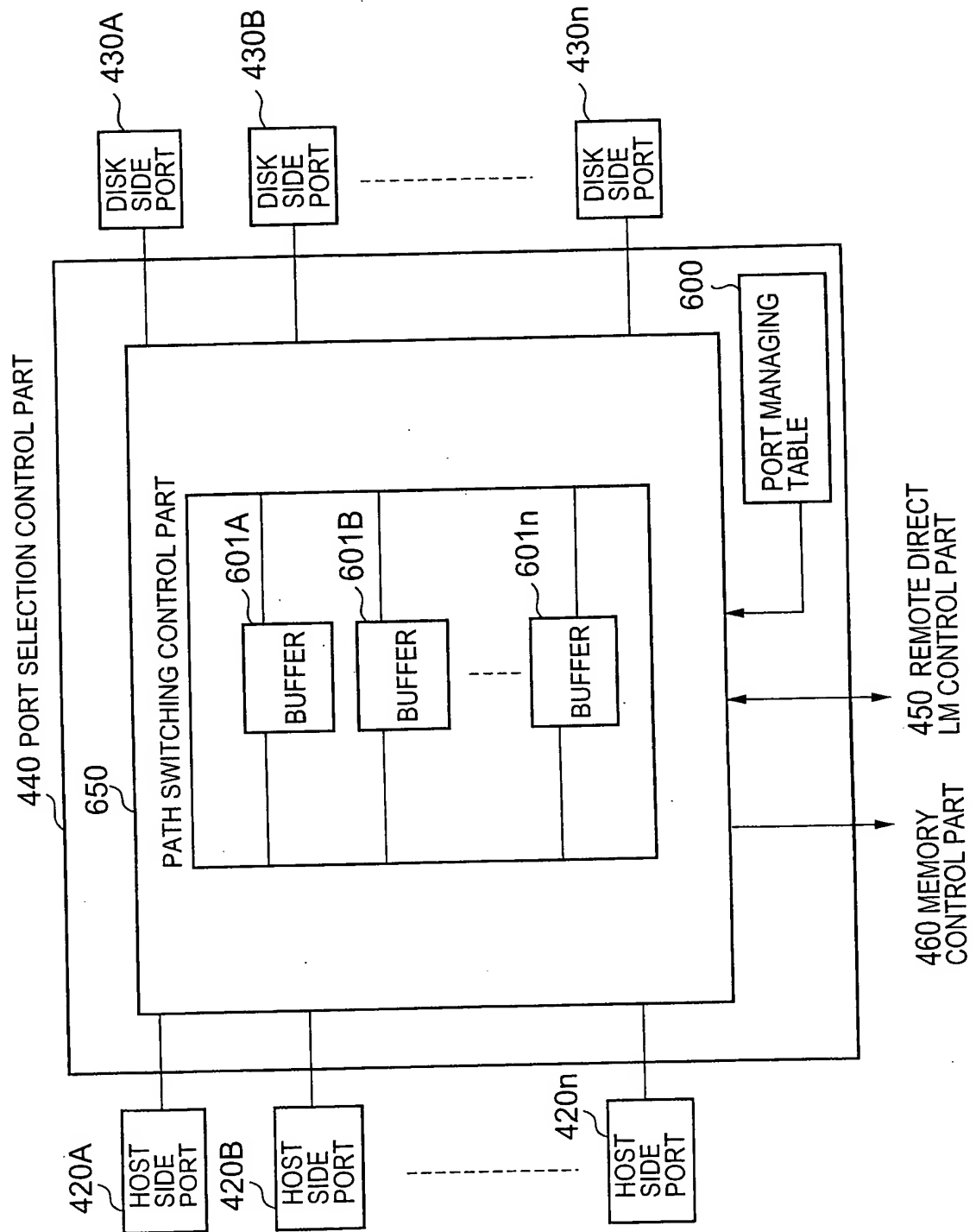
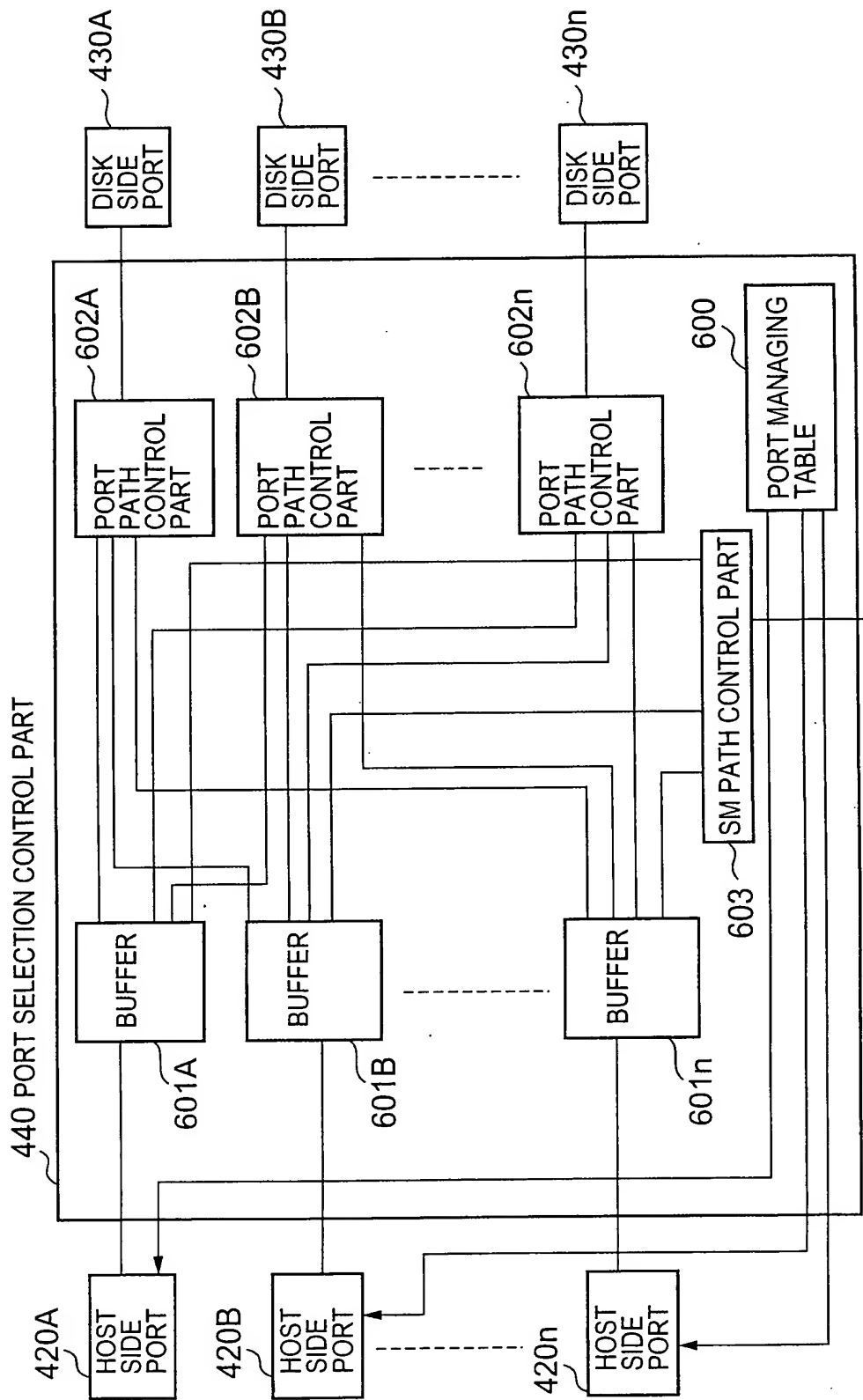


FIG. 10



LM ADDRESSES AND MESSAGES IN DIRECT LM WRITE COMMANDS
TO MEMORY CONTROL PART 460 AS DATA
LM ADDRESSES IN COMMANDS TO ADDRESS DECODER 451

FIG. 11

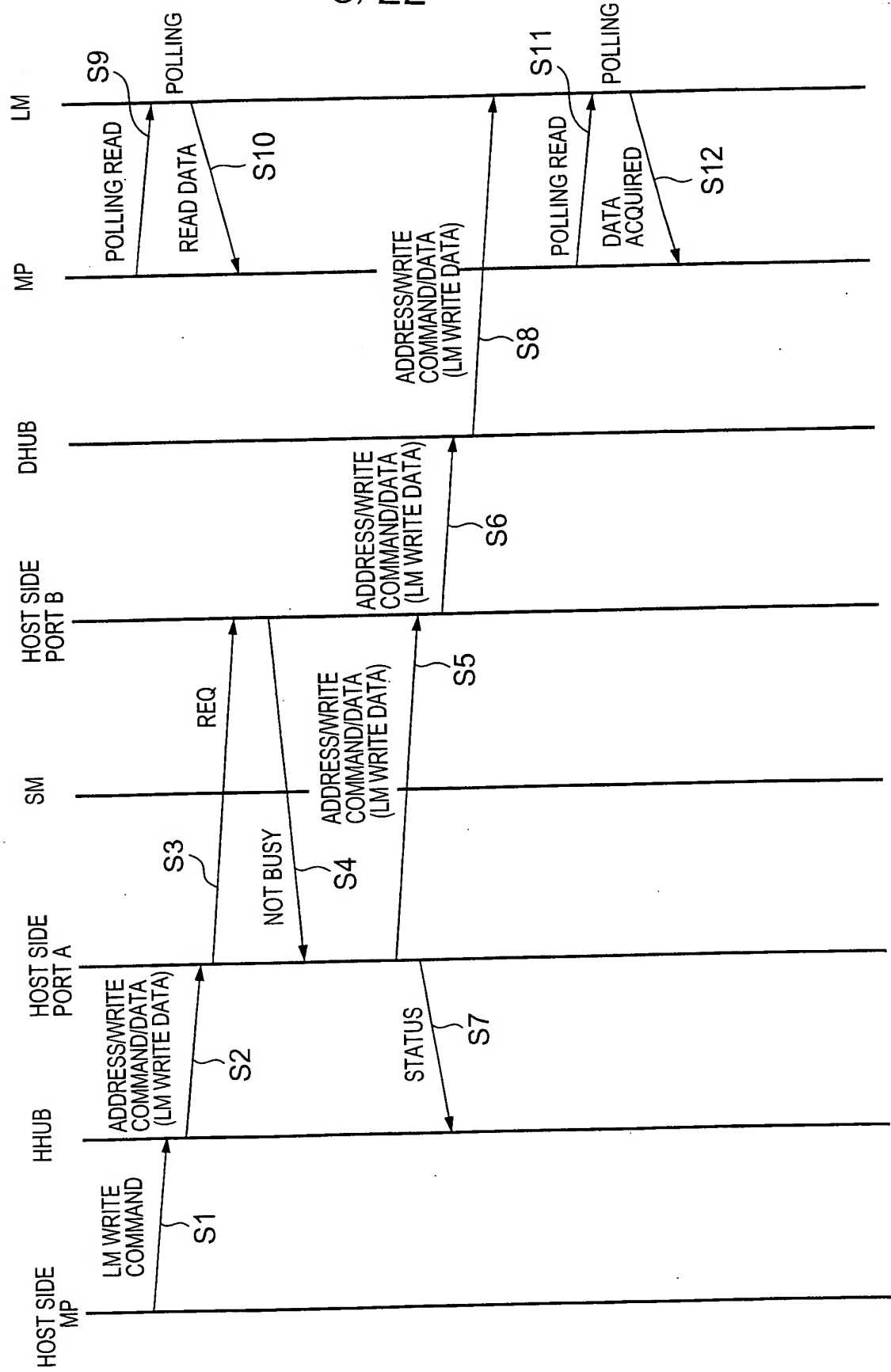


FIG. 12

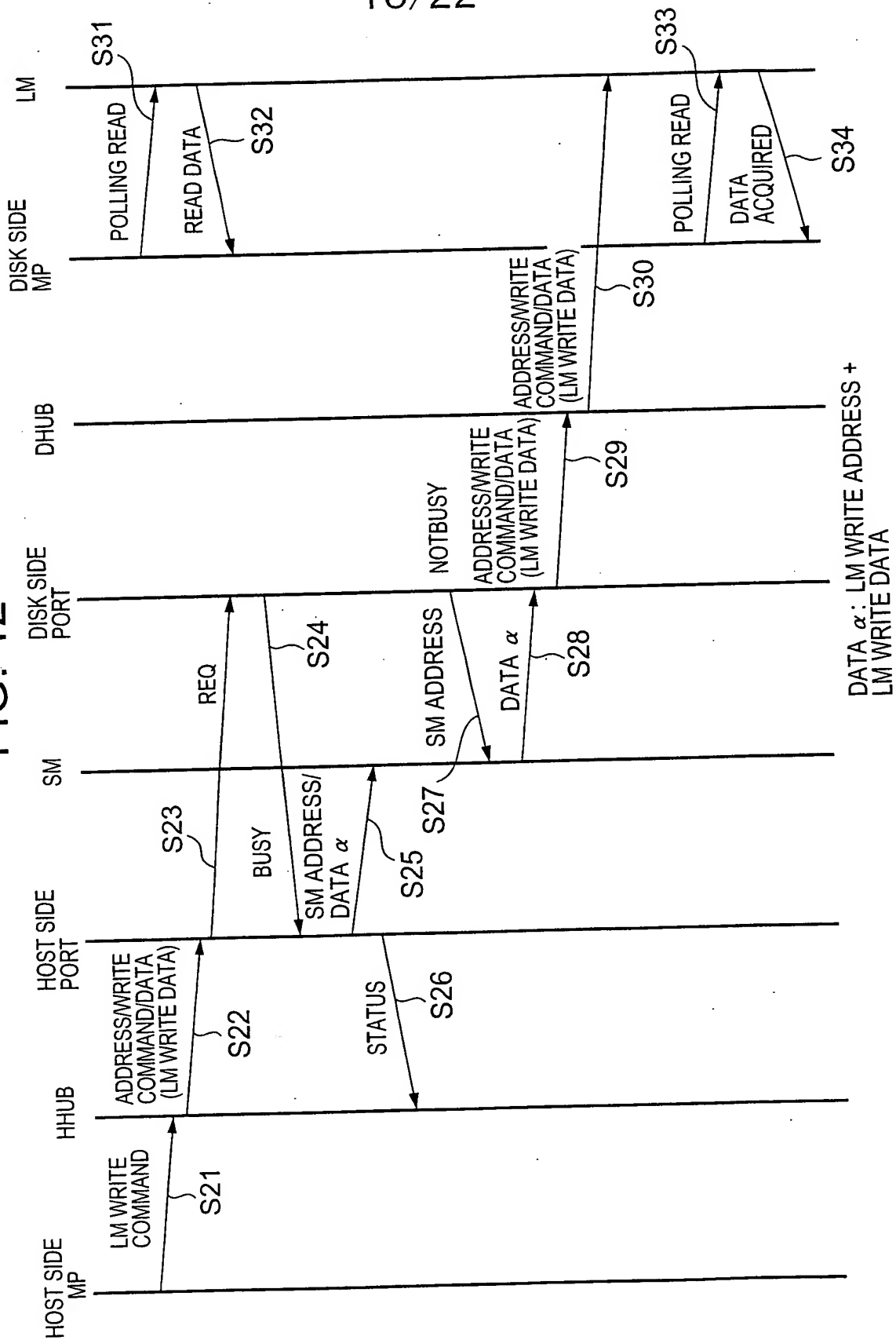
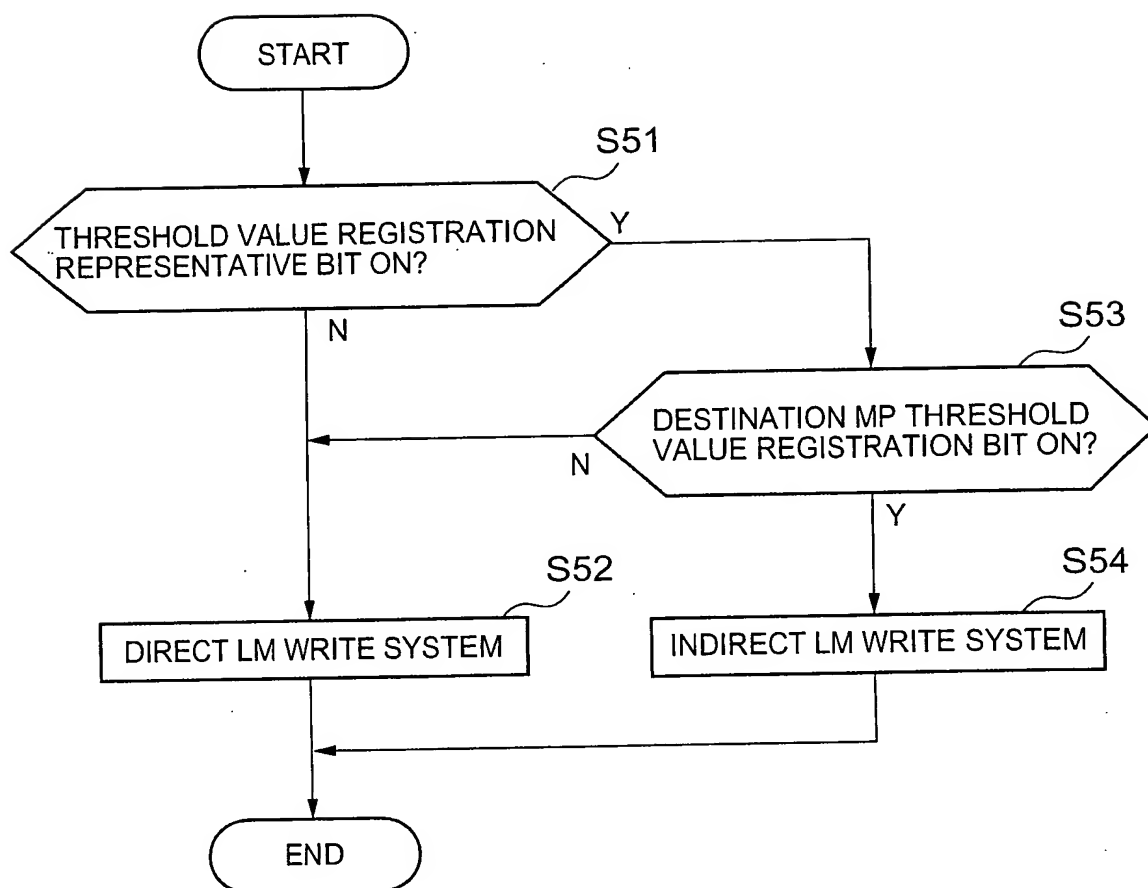


FIG. 13



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FIG. 14

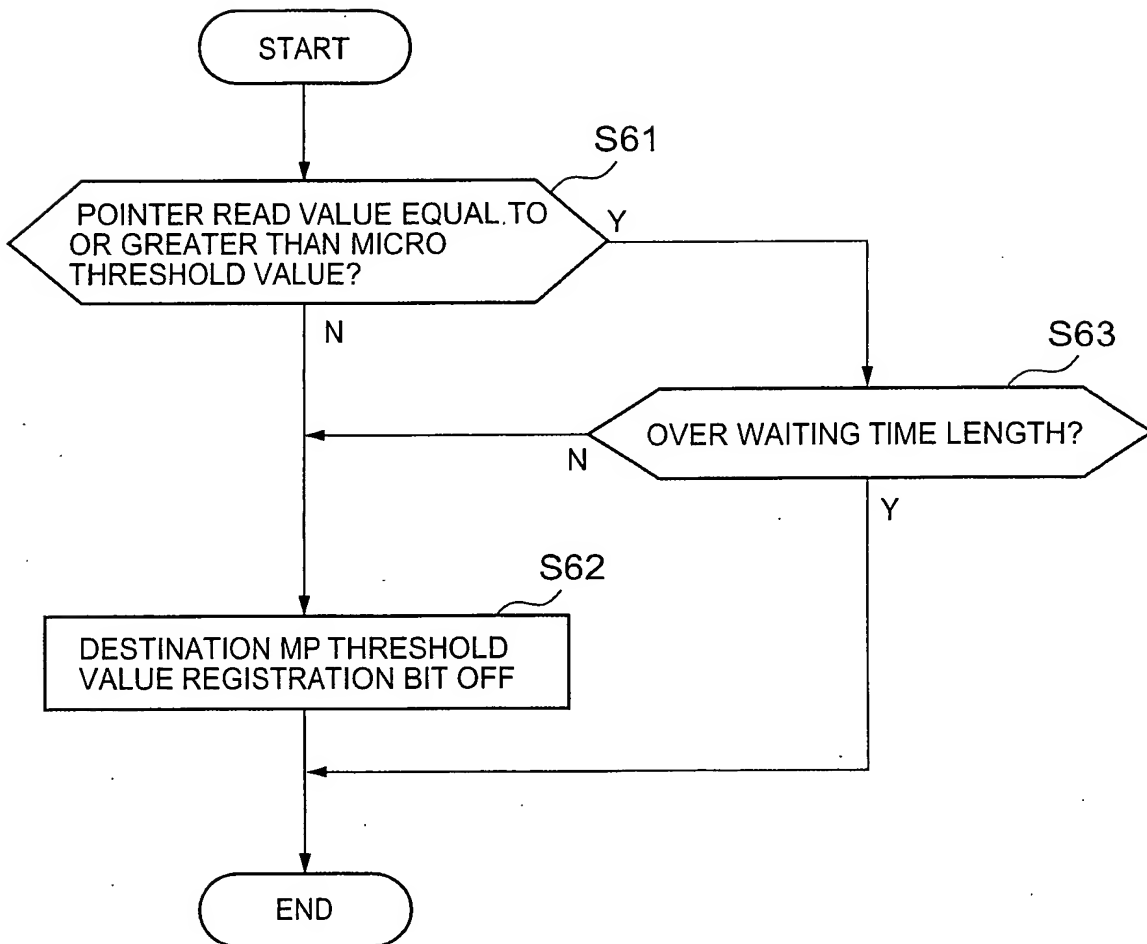


FIG. 15

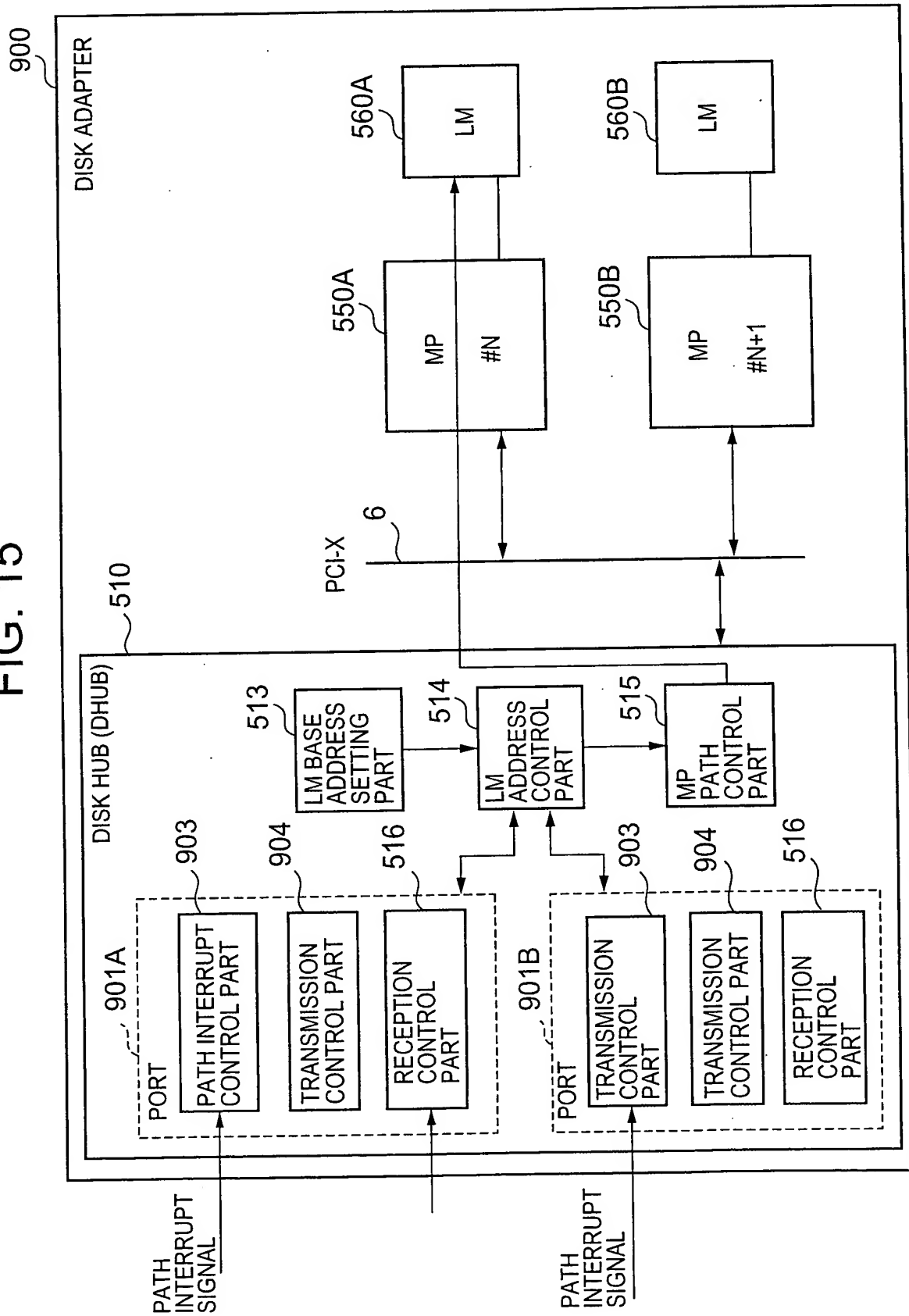


FIG. 16

ADDRESS INFORMATION (INT ADDRESS)	COMMAND TYPE (READ REQUEST)	CRC
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FIG. 17

READ DATA (LM ADDRESS)	READ DATA (WRITE DATA)	CRC
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FIG. 18

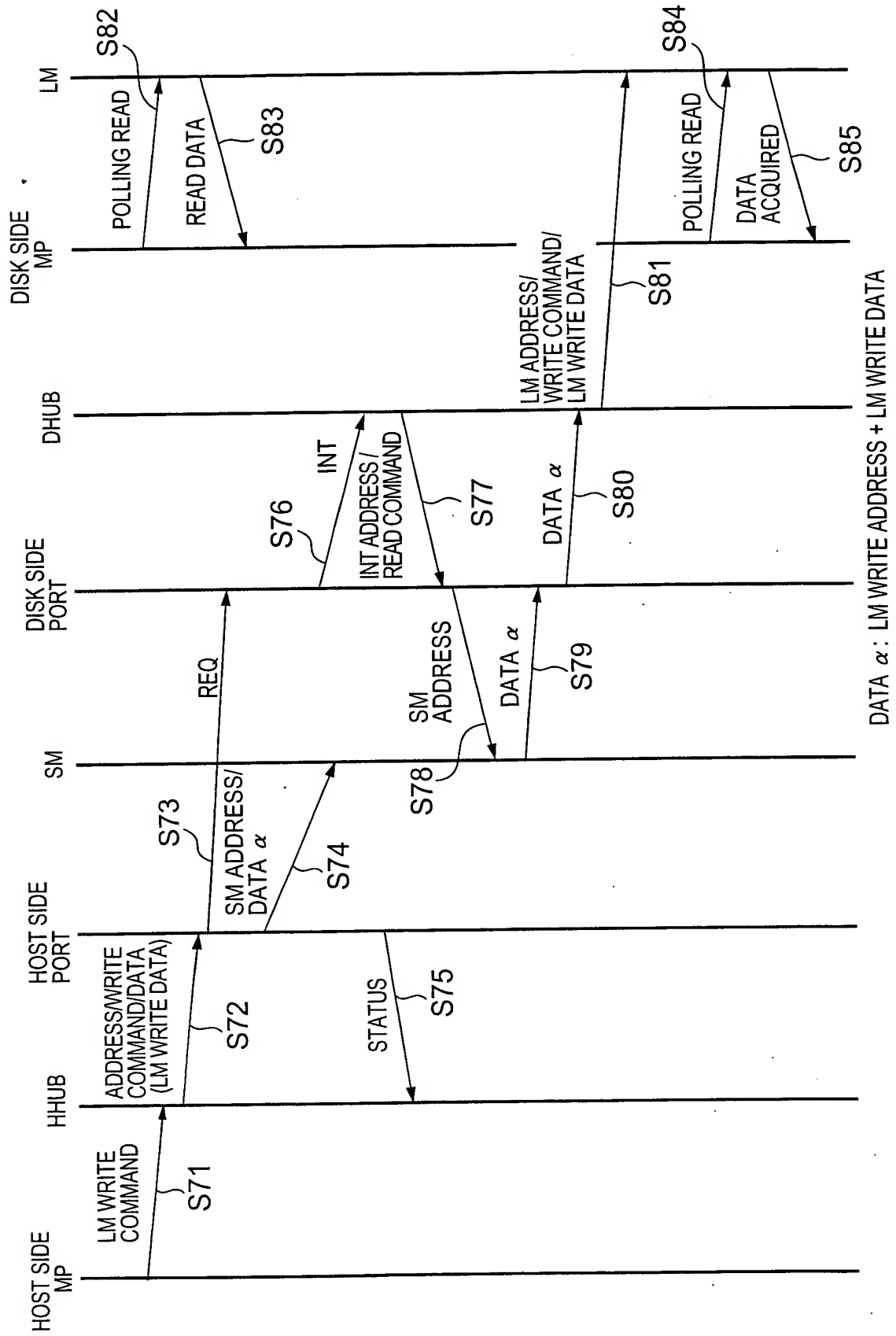


FIG. 19

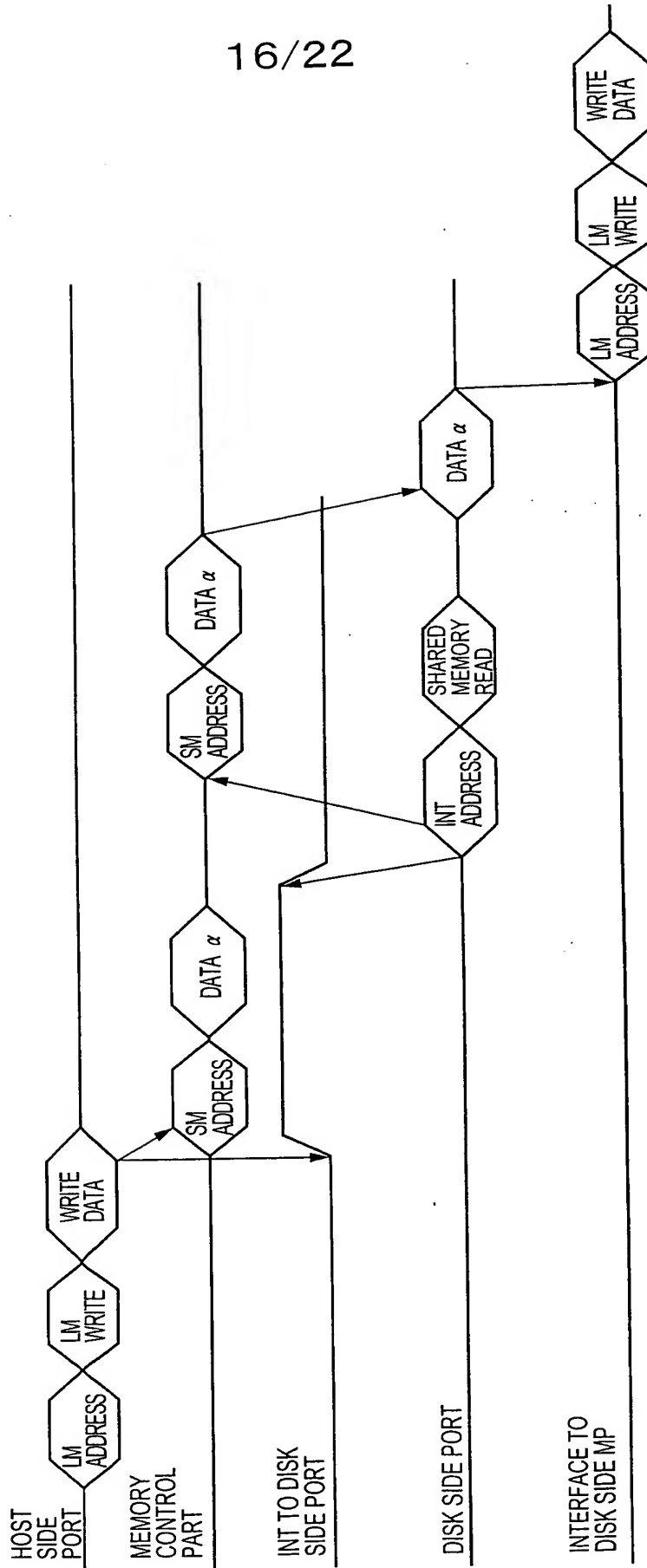


FIG. 20

MP360 (MP # "00")

ADDRESS	USE
0x0000 0000	PCIX OR OTHER DEVICE
0x1000 0000	SMA I/O REGISTER
0x2000 0000	TRANSMISSION SM ADDRESS INFORMATION
0x3000 0000	RECEPTION SM ADDRESS INFORMATION
0x4000 0000	LM WRITE ADDRESS INFORMATION FOR OTHER MP
0x5000 0000	LM WRITE/READ ADDRESS INFORMATION FOR OWN MP
0x6000 0000	LM READ ADDRESS INFORMATION FOR OTHER MP

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START	NAME OF ADAPTER	OTHER MP #	OTHER LM WRITE ADDRESS
0x4000 0000	CHA0	00	0x0000 0000
0x4100 0000		01	0x0000 0000
0x4200 0000	CHA1	02	0x0000 0000
0x4300 0000		03	0x0000 0000
0x4400 0000	CHA2	04	0x0000 0000
0x4500 0000		05	0x0000 0000
0x4600 0000	CHA3	06	0x0000 0000
0x4700 0000		07	0x0000 0000
0x4800 0000	DKA0	08	0x0000 0000
0x4900 0000		09	0x0000 0000
0x4A00 0000	DKA1	0a	0x0000 0000
0x4B00 0000		0b	0x0000 0000
0x4C00 0000	DKA2	0c	0x0000 0000
0x4D00 0000		0d	0x0000 0000
0x4E00 0000	DKA3	0e	0x0000 0000
0x4F00 0000		0f	0x0000 0000

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START	NAME OF ADAPTER	OTHER MP #	OTHER LM READ ADDRESS
0x4000 0000	CHA0	00	0x0000 0000
0x4100 0000		01	0x0000 0000
0x4200 0000	CHA1	02	0x0000 0000
0x4300 0000		03	0x0000 0000
0x4400 0000	CHA2	04	0x0000 0000
0x4500 0000		05	0x0000 0000
0x4600 0000	CHA3	06	0x0000 0000
0x4700 0000		07	0x0000 0000
0x4800 0000	DKA0	08	0x0000 0000
0x4900 0000		09	0x0000 0000
0x4A00 0000	DKA1	0a	0x0000 0000
0x4B00 0000		0b	0x0000 0000
0x4C00 0000	DKA2	0c	0x0000 0000
0x4D00 0000		0d	0x0000 0000
0x4E00 0000	DKA3	0e	0x0000 0000
0x4F00 0000		0f	0x0000 0000

FIG. 21

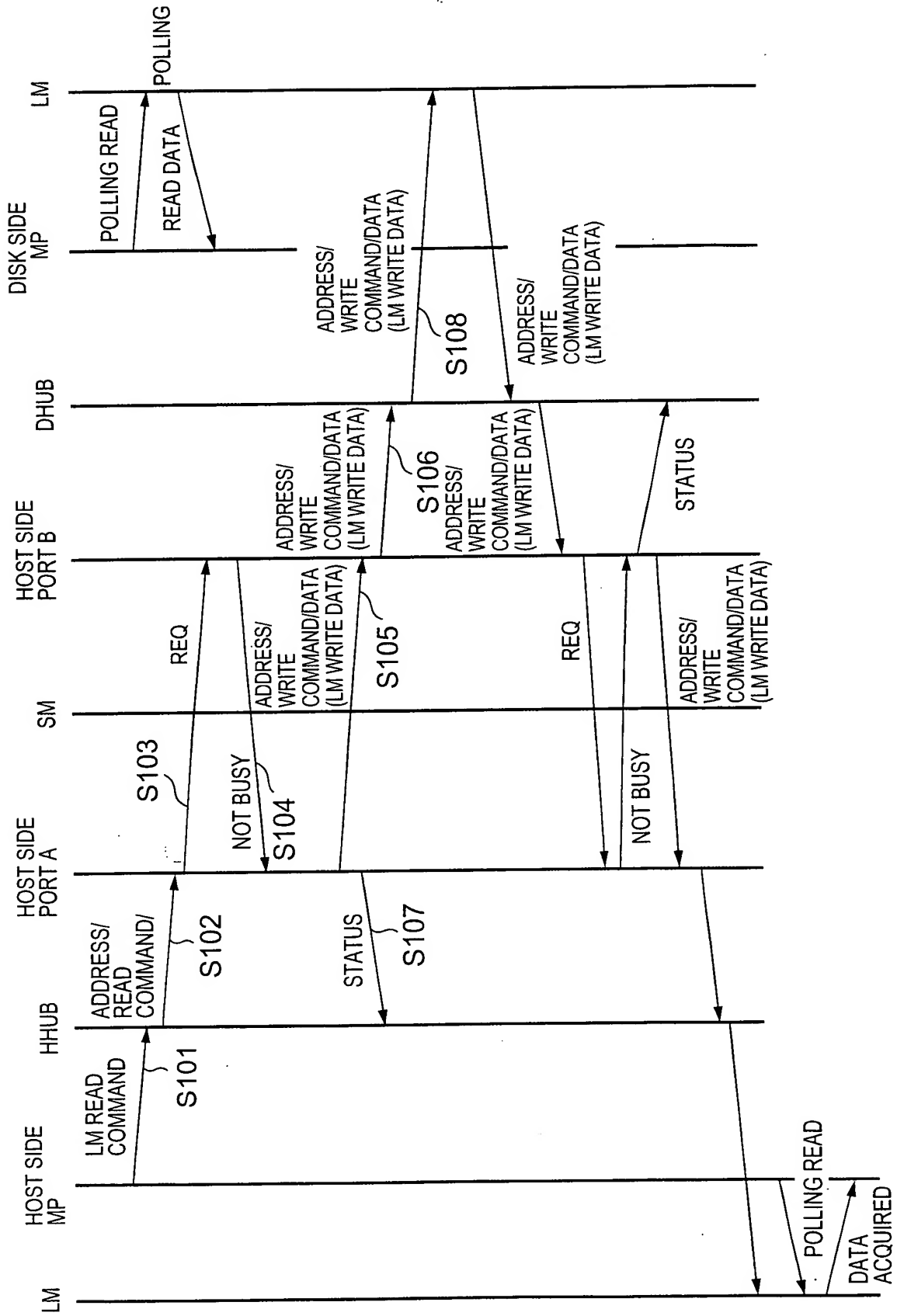


FIG. 22

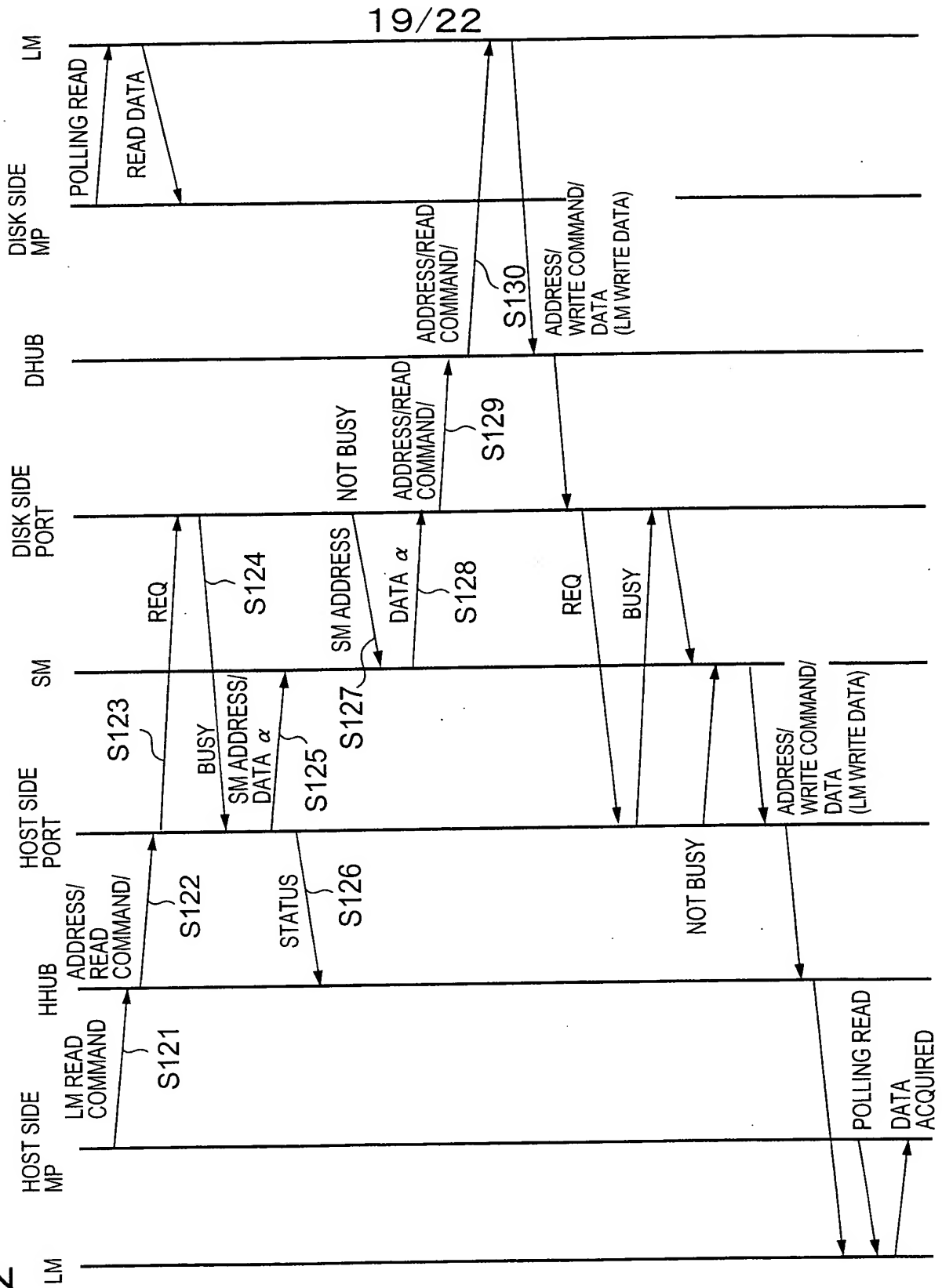


FIG. 23

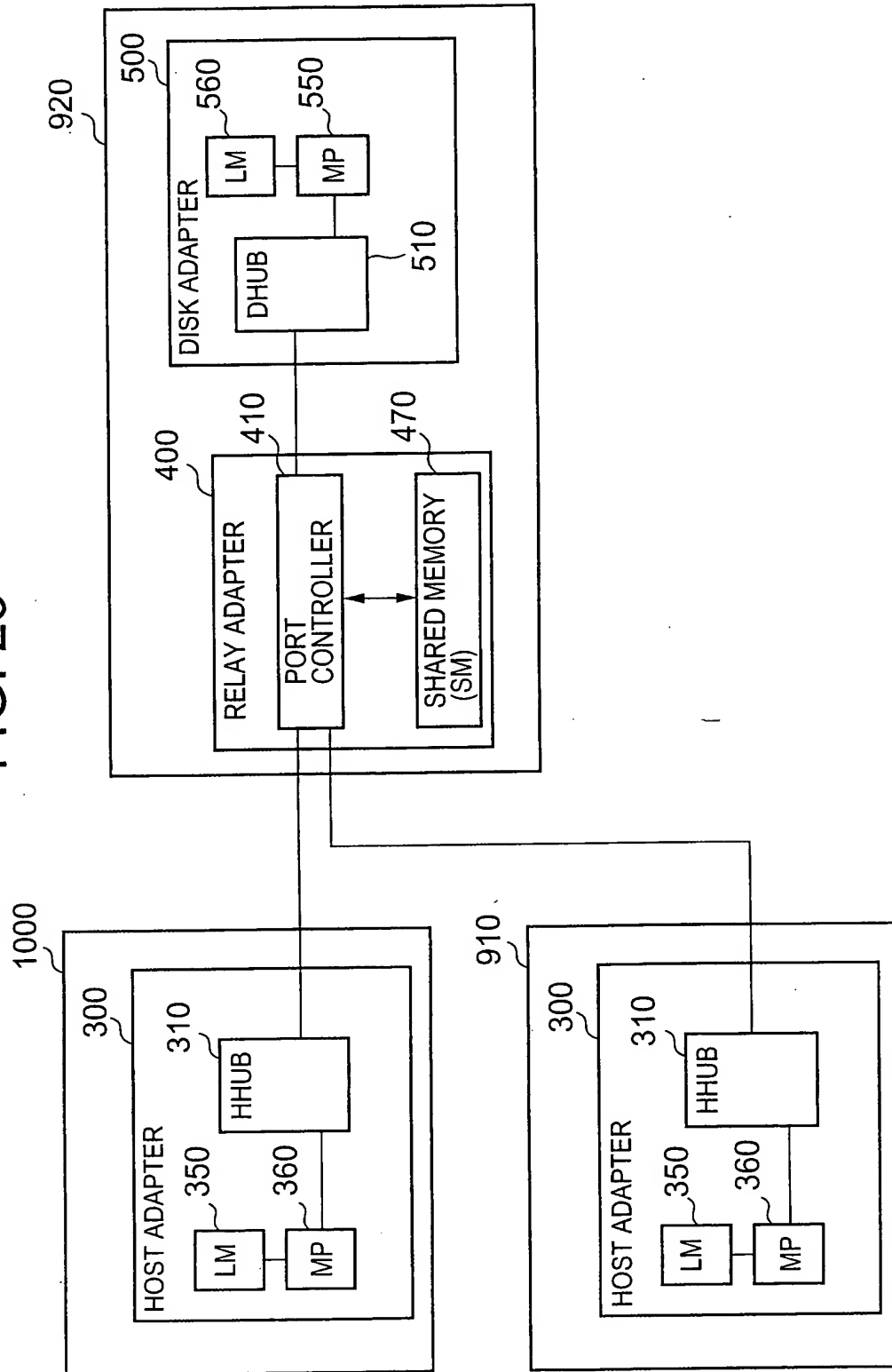


FIG. 24

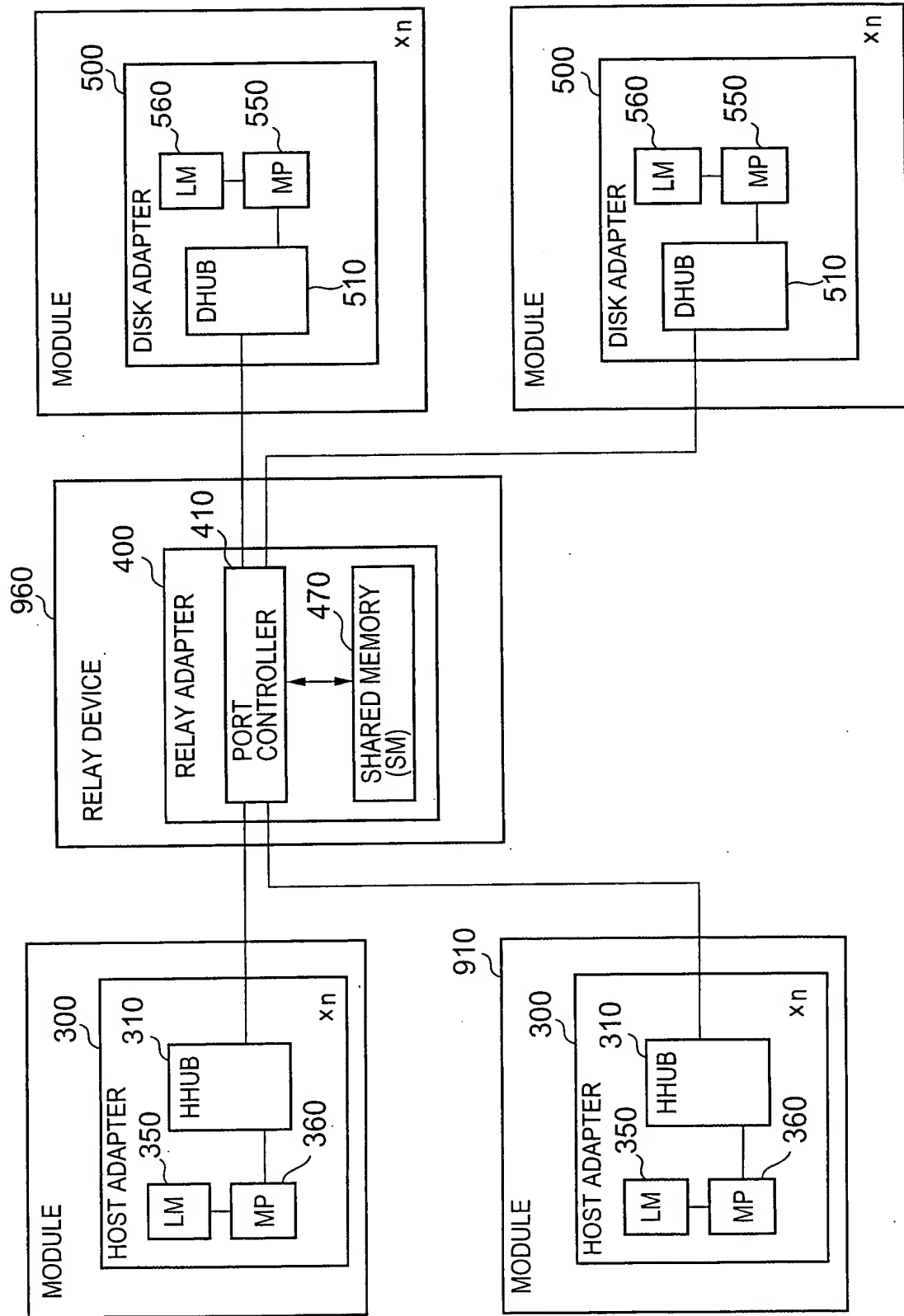


FIG. 25

